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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Applicati n Number	09/928,388
		Filing Date	08/13/2001
		First Named Invent r	Koy B. Cook, Jr.
		Group Art Unit	
		Examiner Name	
Sheet 1	of 1	Attorney Docket Number	00-0754.02/US/2

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
PL	1	VENLER, LANDOLT, DEBERGH, ARREGUIT, "Analog CMOS Photosensitive Array for Solar Illumination Monitoring," Solid State Circuit Conf.,	

Examiner Signature	<i>Pat 1 L</i>	Date Considered	2/2/2004
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